Building a Safe and Robust Industrial System with Avago Technologies' Optocouplers

White Paper

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Introduction

Should production line stoppage occur at an industrial automation company, millions of dollars and opportunity costs can be lost while trying to fix the problem and get the lines running again.

Breakdown can occur in areas such as electromagnetic interference, high voltage surges, and safety standards. These areas of concern need to be checked at the initial design level rather than later, after manufacturing has been developed and built.

The harsher environment on the factory floor poses issues not relevant in an office environment. With the advent of the ethernet[1] integration into the industrial automation arena, it has become much more relevant to have stringent requirements in the Fieldbus and device levels for data collection at the receiver end.

Optocouplers are used extensively in industrial networking systems for numerous purposes. They allow electrical circuits and highly diverse voltage levels to work together as a system and can be coupled while remaining electrically isolated or galvanically[2] separated from one another. They are also used to ensure error-free data transmission, retain data integrity, and protect interconnected equipment for high-speed Fieldbus communications. Usage of optocouplers in industrial communication applications includes industrial input-output systems, sensors and temperature controlling systems, power supplies and regulation systems, electric motor control and drive systems, instrumentation and medical systems.

This white paper discusses the important factors that need to be taken into consideration when building a safe and robust industrial system.

1 **Ethernet:** A technology that interconnects computers into a high-speed network originally developed by Xerox Corporation. Ethernet is widely used for LANs because it can network a wide variety of computers. It is not proprietary, and components are widely available from many commercial sources.

2 **Galvanic Isolation:** Refers to a design or material techniques that guarantee voltage and noise isolation across an insulating barrier.

Optocoupler Basics

A basic optocoupler consists of a light-emitting diode (LED), a photodetector, and an optically transparent, electrically insulating film or dielectric. When a current drives the LED, it emits light, which is coupled to the photodetector through the dielectric. The photodetector generates a current that is proportional to the coupled light. This current can be manipulated by various circuitry to perform specific functions. The major function of an optocoupler is to prevent high voltages or rapidly changing voltages on one side of the circuit from damaging components or distorting transmissions on the other side. This is done by optically passing desired signals, while maintaining electrical isolation between the two systems.

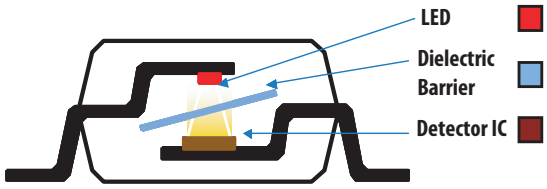

Figure 1. Cross-sectional area of Avago Technologies' optocoupler

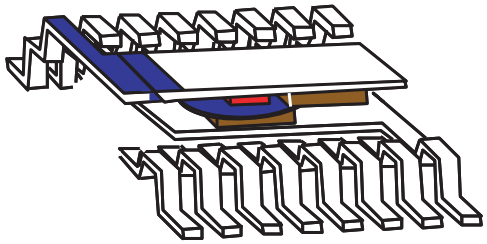

Figure 2. Side view of an optocoupler

Circuit designers, when designing their applications, can encounter three types of isolation related issues:

- **Voltage Transients**: These are potentially **high current or voltage surges** that may damage components and cause electric shock, possibly endangering human life. They are usually brief and intense surges between two circuits or systems.
- **Ground Loop Currents**: These are unwanted signals between interconnections of different ground potentials, which cause disruptive ground loops. They are usually found in communication networks having **different grounds** at various connecting nodes. The potential difference between these grounds can be alternate current (AC) or direct current (DC), with a combination of various noise components found in that communication system. If the voltage potential is large enough, it may cause damage to equipment (e.g., communication ports), transmission error, or degradation of data signals. Long term exposure results in the heating and burning of circuit boards which damages components and causes electric shock, some potentially deadly to human beings.
- **High-voltage Level Shifting**: With the migration of digital ICs to lower operating voltages, the need for devices to separate sensitive electronics from high power electronics is growing. In order to ensure reliable information exchanges and prevent current flow between different ground reference voltages, there is a need to use isolation. For example, in a motor control application, the electronic system of a motor consists of 2 stages, the low voltage controller and the power module. Within such a system, it is important to protect and insulate the two stages from switching transients and common mode voltage fluctuations. At the same time, it is necessary to provide **level shifting** and **signal isolation** of interface control and feedback circuits.

(A) Safety Standards for Isolation Devices

International Safety Standards are published to ensure equipment and products are used at a basic standard level of safety—not only for the equipment, but also for the operators. These standards are focused on public safety in the areas of electrical shock, mechanical hazards and fire, and electromagnetic interference. At the system and component levels, there are many isolation safety standards, both geographically and within the various equipment applications. In the industrial market, the system level safety standards are IEC 604[3] (International Electrotechnical Commission) for Worldwide or International standards, and UL508[4] (Underwriters Laboratories) for the United States and EN 50178[5] (European Union) for Europe. At the component level for optocouplers, the safety standards are **IEC 60747-5-2** for International, **UL 1577** for United States and **EN 60747-5-2** for Europe.

For future optocoupler standards and maintenance, the IEC will become the de-facto standard worldwide. To receive IEC 60747-5-2 approval, optocoupler components undergo a stringent set of qualification tests that include environmental, mechanical, isolation, and electrical testing. The criterion for passing the component is the **Partial Discharge** (PD) test with a rigorous upper limit of 5pC.

Insulation, as a resistor to current flow, is an important factor in product safety design. The fundamental principle of designing for product safety is the separation of circuits that present a danger of electrocution from other circuits, or certain parts of the equipment which a user may come into contact with, or which connects to other equipment. The circuit must be safe, not only during normal usage but also under fault conditions. Two main levels of insulation with clear safety distinctions are '**Basic Insulation**'[6] and '**Reinforced Insulation.**'[7]

- **Basic Insulation**

  Since January 2004, the German safety standard certification for optocouplers VDE 0884 has been replaced by **IEC/EN/DIN EN 60747-5-2,**[6] This new safety standard is directly applicable to optically isolated devices. Although this standard specifically pertains to optical isolators only, devices using other isolation technologies, such as magnetic or capacitive isolation barriers, have also surprisingly and perhaps erroneously obtained certifications to this optocoupler safety standard. This recognition is limited to '**Basic Insulation**' only. This level of insulation may not provide '**failsafe operation.**'

  Devices that are certified and approved under IEC/EN/DIN EN 60747-5-2 with recognition for '**Basic Insulation**' only provide basic protection against electrical shock. They cannot be considered as '**failsafe.**'[9] Such devices should not be accessible to a user.

- **Reinforced Insulation**

  The level of insulation required is very much dependent on the failure mode of a component under fault conditions. '**Reinforced Insulation**' is only approved for a '**failsafe**' component. This means that '**Reinforced Insulation**' not only provides protection from electric shock, its **failsafe** design permits user accessibility.

  Avago Technologies has manufactured optocouplers for more than 30 years, with a wide range of product offerings from phototransistors to the industry's fastest optocoupler at 50MBd. Avago Technologies offers one of the highest insulation working voltages at 1414Vpeak. All Avago Technologies' optocouplers are approved and recognized by component-level safety standards; these include UL1577 (Underwriters Laboratories), CSA (Canadian Standard Association) and IEC/EN/DIN EN 60747-5-2. The UL and CSA ratings are based on a momentary dielectric withstand voltage capability of one minute, while the IEC/EN/DIN EN ratings are based on continuous working voltages and transient over-voltages.

3 **IEC 604:** Industrial international standard for equipment and machinery (Web link http://www.iec.ch)

4 **UL 508:** US Industrial standard for machines (Web link http://www.ul.com/)

5 **EN 50178:** European standard for industrial equipment (Web link http://www.newapproach.org/)

6 **Basic Insulation:** Insulation applied to live parts to provide basic protection against electric shock (http://www.601help.com/Disclaimer/glossary.html)

7 **Reinforced Insulation:** Single insulation system applied to live parts which provide a degree of protection against electric shock equivalent to double insulation under the conditions specified in IEC 60601-1. (http://www.601help.com/Disclaimer/glossary.html)

8 **IEC/EN/DIN EN 60747-5-2:** International safety standards (Web link http://www.cenelec.org/)

9 **Failsafe:** A mode of system termination that automatically leaves system processes and components in a secure state when a failure occurs or is detected in the system. (http://homepage.mac.com/antallan/gistf.html)

As shown in Figure 3, Avago Technologies' optocouplers provide a 'failsafe' level of high voltage isolation as indicated in the datasheets for all the optocoupler product offerings.

IEC/EN/DIN EN 60747-5-2 Insulation Related Characteristics (Option 060)

Description	Symbol	HCPL-772X Option 060	HCPL-072X Option 060	Units
Installation classification per DIN VDE 0110/1.89, Table 1 for rated mains voltage ≤ 150 V rms for rated mains voltage ≤ 300 V rms for rated mains voltage ≤ 450 V rms		 I-IV I-IV I-III	 I-IV I-III	
Climatic Classification		55/85/21	55/85/21	
Pollution Degree (DIN VDE 0110/1.89)		2	2	
Maximum Working Insulation Voltage	V_{IORM}	630	560	V peak
Input to Output Test Voltage, Method b† V_{IORM} x 1.875 = V_{PR}, 100% Production Test with t_m = 1 sec, Partial Discharge < 5 pC	V_{PR}	1181	1050	V peak
Input to Output Test Voltage, Method a† V_{IORM} x 1.5 = V_{PR}, Type and Sample Test, t_m = 60 sec, Partial Discharge < 5 pC	V_{PR}	945	840	V peak
Highest Allowable Overvoltage† (Transient Overvoltage, t_{ini} = 10 sec)	V_{IOTM}	6000	4000	V peak
Safety Limiting Values (Maximum values allowed in the event of a failure, also see Thermal Derating curve, Figure 11.) Case Temperature Input Current Output Power	 T_S $I_{S,INPUT}$ $P_{S,OUTPUT}$	 175 230 600	 150 150 600	 °C mA mW
Insulation Resistance at T_S, V_{IO} = 500 V	R_{IO}	≥ 10^9	≥ 10^9	Ω

†Refer to the front of the optocoupler section of the *Isolation and Control Component Designer's Catalog*, under Product Safety Regulations section IEC/EN/DIN EN 60747-5-2, for a detailed description.

Note: These optocouplers are suitable for "safe electrical isolation" only within the safety limit data. Maintenance of the safety data shall be ensured by means of protective circuits.

Note: The surface mount classification is Class A in accordance with CECC 00802.

suitable for "safe electrical isolation"

Figure 3. Example of standard compliance in Avago Technologies' optocoupler products

(B) Reliability of High Voltage Insulation

Optocouplers are often used in environments where high voltages are present. Though many safety standard regulations have been established to provide guidelines on the application of high voltages, reliability concerns remain due to poorly understood aging and failure mechanisms as a result of electrical and thermal stress.

Evaluation testing was carried out recently to determine the length of time an isolation device will successfully insulate one side of the isolation barrier from high voltages on the other side. The testing was performed to assess the reliability of a device in the areas of high voltage performance and insulation integrity. The high voltage life test performed was defined as a destructive test since high voltages of 2.5KV and 3.75KV (specified on Avago Technologies' datasheets) were applied constantly to Competitor A devices and Avago Technologies' optocouplers, respectively. The lifespan of the units was monitored hourly until the isolation barriers were broken down, or until the test units were destroyed.

Several Competitor A magnetic isolator parts were randomly selected for the test. As shown in Table 1, the units were destroyed between 8.5hrs to 10.5hrs. Avago Technologies' optocouplers survived a minimum duration of 168hrs of high voltage at 3.75KV. This is verified through our reliability testing process.

Table 1. High Voltage Life Test Results

	High Voltage	
	2.5KV	3.75KV
	Magnetic Isolators	Optocouplers
Competitor A 3-channels device	Failed at 8.5hrs	
Competitor A 4-channels device	Failed at 10.5hrs	
Avago Technologies' Digital High-Speed CMOS part (25MBd)		Still working at 168hrs
Avago Technologies' High-Speed CMOS part (50MBd)		Still working at 168hrs

(C) Electrostatic Discharge

One of the primary causes of component failure in high-speed logic circuits is **Electrostatic Discharge (ESD)**. ESD occurs in various situations, from improper device or board handling, improperly designed interfaces, or some other phenomenon that causes a large voltage spike on a device interface. When devices are damaged by ESD, the affected devices may cease to function, exhibit parameter degradation, or demonstrate high failure rates. The only solution is the replacement of the damaged component.

Optocouplers are excellent devices for protecting against ESD problems, especially in situations where two systems are being linked together in electrically demanding environments. Optocouplers allow for ground isolation, making it possible for systems to remain electrically neutral within themselves even though they may be floating in an electrically noisy environment. These environments can include motor control, switching power supplies, industrial networks, and medical applications.

An ESD test was carried out recently to assess and evaluate the performance of optical and magnetic technologies. The test evaluated the impact of ESD pulses applied onto the dielectric materials of two different technologies. Abiding by the testing requirements of the IEC-6100-4-2 standard, five randomly selected magnetic isolators from Competitor A underwent ESD pulses injected into their input side, while all the pins were shorted together on both the input and output sides, as shown in Figure 4, Step 1. The resistance between the input and the output was then measured (Figure 4, step 2) and the results were tabulated in Table 2.

- **Test Result**

  As mentioned in the previous section, the test was carried out by injecting ESD pulses into the input side of five units from Competitor A. The ESD voltage level was increased from 5.5KV in steps of 0.5KV, until the units failed or broke down. Upon measuring the resistance across the input and output, the readings were close to zero ohms. This implied that the devices under test short-circuited. Three Avago Technologies' optocouplers were then tested using the same method. The result indicated that Avago Technologies' optocouplers showed no dielectric failure, even up to an ESD voltage level of 11KV. At ESD voltage levels of about 11.5KV, external arcing did occur on the optocouplers which prevented further testing.

  Table 2 shows that the dielectric failure occurred for three of the five units of Competitor A parts at approximately 10KV, while the other two units failed at an even lower ESD voltage levels of 6.5KV and 8.0KV, respectively. This indicated that Competitor A might be more prone to ESD stress than Avago Technologies' optocouplers. The test also showed that the insulating capability of the optocouplers was undisturbed by the ESD stress throughout the testing.

  To investigate the cause of failure for the parts under test, the units were decapsulated. Burn marks were found on the transformer IC and the driver IC (see Figure 5). ESD caused a 'punch through' effect, resulting in damage to these magnetic devices.

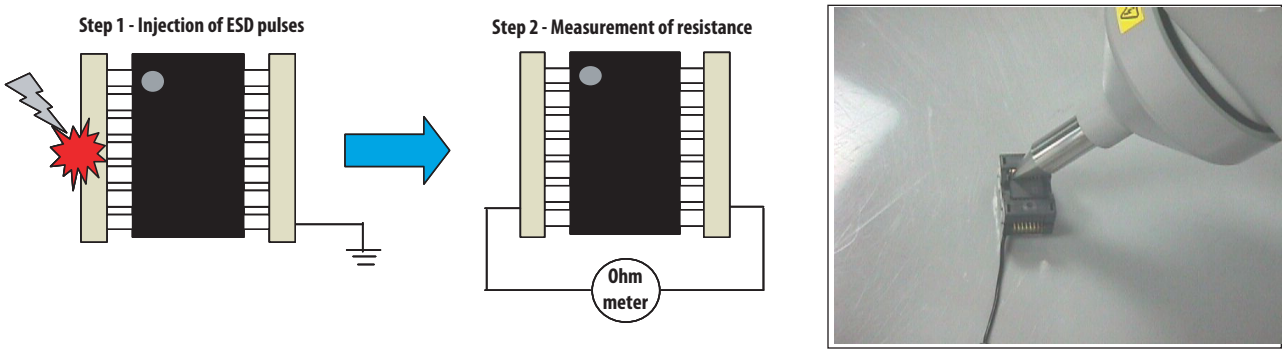

Figure 4. ESD Testing

Table 2.

Competitor A - Magnetic Isolators

Unit	Competitor A KV	Resistance (Gnd-to-Gnd) Kohm	Comment
1	6.5	4.7	Shorted
2	10.0	9.3	
3	10.0	191.2	
4	8.0	11.1	
5	10.5	13.9	

Avago Technologies' HCPL-314J optocouplers

S/N	HCPL-314JKV	Resistance (Gnd-to-Gnd) Kohm	Comment
1	11.5	-	External arcing occurs
2	11.5	-	
3	11.5	-	

Driver IC – Burn

Transformer IC – Burn

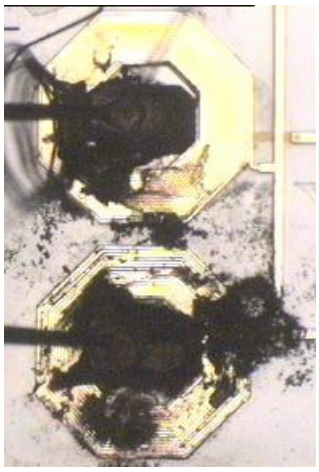

Figure 5. Burn mark on Competitor A parts

(D) Electromagnetic Interference (EMI)

Electromagnetic interference (EMI) can be defined as any electromagnetic disturbance that disrupts, degrades, or otherwise interferes with authorized electronic emissions limiting the effective performance of electronics and electrical equipment. It can be induced intentionally, as in some form of electronics warfare, or unintentionally as a result of spurious emissions and responses, intermodulation products, atmospheric disturbances (including lightening) and extra-terrestrial sources (such as sunspots). Radio frequency interference (RFI) is a special class of EMI in which radio frequency transmissions (usually narrow-band) cause unintentional problems in equipment operation. Radio frequency interference can originate from a wide range of sources such as mobile phones or power lines, transformers, medical equipment, electromechanical switches, and many others that can be found in industrial environments.

There are two forms of EMI: **Radiated EMI** and **Conducted EMI**. While Radiated EMI is interference that travels from a source, through the air, to the receiving source, Conducted EMI travels along a conducting path. Both can lead to transmission of unwanted electronic signals. This interference can propagate the authorized signals, which can interfere in the proper operation of the equipment or device by altering normal operating parameters. These failures are generally categorized as **Electromagnetic Interference** or EMI failures.

Addressing EMI issues is a challenge. When electromagnetic interference is suspected, the first step in resolving the problem is to determine the mechanism for energy transfer to the affected device(s): radiation, conduction, or induction. Improvements can be achieved by limiting the amount of induced energy either by removing the root cause (physical separation) or by protecting the failing device, e.g., by shielding in the telecommunication area. The best way to avoid potential EMI problems is by choosing less sensitive or immune devices, by optimizing the layout to minimize coupling effects and proper shielding.

All of the various isolators and couplers on the market consist of integrated CMOS or bipolar ICs. The coupling unit, which is the main differentiator among the different technologies available today, can be optically coupled isolators (optocouplers), magnetic coupled isolators (magnetic couplers), and capacitive coupling isolators (capacitive couplers). Each of these behave differently in the presence of strong electromagnetic fields. While the optocoupler LED/photodiode combination is known to be immune to electromagnetic interferences due to the optical coupling path, the magnetic isolators have limitations with respect to EMI due to their microstructure and the magnetic coupling. Failures of the magnetic couplers can occur at the DC level of the field (0 Hz), as well as at various frequencies at different levels of field strength.

The key consideration for designers is to avoid potential EMI problems in their applications or equipment used in an industrial environment or in close proximity to the motor control. Optocouplers are the best choice, as they provide superior EMI performance and can withstand much higher electromagnetic fields compared to all other isolators currently available on the market.

Summary and Conclusion

In designing a good industrial system, equipment and component safety plays an important part and is one of the main considerations, especially when high voltages (above DC 48V, AC 110) are involved. These types of systems are usually surrounded by motor starters, servo drives, programmable logic controllers, and/or power converters. Providing a safe work environment for personnel plays a vital role in system design. System critical applications are expected to be failsafe.

Avago Technologies' optocouplers have proven to provide reliable and failsafe parts to meet necessary application requirements. We have discussed four factors when designing a safe and robust industrial system:

1. The various safety standards for isolation devices. (Avago Technologies' optocouplers have 'Reinforced Insulation', which provides failsafe operations.)
2. Reliability of high voltage insulation, which minimizes the frequency of component breakdown due to high voltage surge into the system. (Avago Technologies' optocouplers can endure a high-voltage of 3.75KV for a minimum of 168Hrs without failure).
3. ESD causes system degradation or malfunction. (Even at an ESD voltage level of 11KV, Avago Technologies' optocouplers did not show any dielectric breakdown failure).
4. EMI is another factor that causes the failure of industrial systems. (Avago Technologies' optocouplers have proven to provide superior performance and are nearly immune to EMI).

While designers usually consider size, low power, and cost in their initial selection for isolation products, the basic requirement for isolation is actually to isolate unwanted signals, while insulating against high voltages.

References

[1] Avago Technologies Regulatory Guide for Isolation Circuits
Web link: http://www.avagotech.com
Publication number: 5989-0342EN
Pages 61 to 69

[2] Avago Technologies High Speed CMOS digital optocoupler datasheet
Web link: http://www.avagotech.com
Publication number: 5989-0790EN
Page 6, IEC/EN/DIN EN 60747-5-2 Insulation Related Characteristic (Option 060)

[3] ESD Considerations in High Speed Circuits
Integrated Device Technology, Inc.
Application Notes AN-123
Web link: http://www1.idt.com/pcms/tempDocs/AN_123.pdf

[4] Isolation and Safety Standards for Electronic Instruments
National Instrument Developer Zone
Web link: http://zone.ni.com/devzone/conceptd.nsf/webmain/6D1C1BE6590C0D4A86256C1A0078763C?opendocument

[5] IEC/UL 60950-1 Application Guideline 3.1.9-1
CENELEC released a standard, EN50116 that defines the routine electrical safety tests and their procedures to be applied during or after the manufacturing process of IT equipment certified or declared as complying with EN60950.
Web link: http://zone.ni.com/devzone/conceptd.nsf/webmain/6D1C1BE6590C0D4A86256C1A0078763C?opendocument

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